

an eighth transistor (Q43) having a collector coupled to an emitter of the second transistor (Q39) and a base of the fourth transistor (Q41), and an emitter being coupled to a second high impedance node operable to sink current from the gain stage;

the fifth transistor (Q36), sixth transistor (Q37), seventh transistor (Q42) and eighth transistor (Q43) sharing the same base-emitter connections; and

the fifth transistor (Q36), sixth transistor (Q37), seventh transistor (Q42) and eighth transistor (Q43) being operably coupled to force current from the fifth transistor (Q36) and the seventh transistor (Q42) to substantially match current from the sixth transistor (Q37) and the eighth transistor (Q43) to substantially match, respectively.

Claim 19 (original): The circuit of Claim ¹⁸~~17~~, further comprising a node capacitively coupling the bases of the first transistor (Q38) and second transistor (Q39) to a voltage rail.

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Claims 20 and 21 (cancelled).

Claim 22 (currently amended): The circuit of Claim 24 18, wherein the first transistor (Q38), fourth transistor (Q41), fifth transistor (Q36) and seventh transistor (Q42) further comprise pnp-type transistors; and

the second transistor (Q39), third transistor (Q40), sixth transistor (Q37), and eighth transistor (Q43) further comprise npn-type transistors.